

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date
21 April 2005 (21.04.2005)

PCT

(10) International Publication Number
WO 2005/036735 A1

(51) International Patent Classification⁷: H03F 3/217, 1/32, 3/217, 1/32, 1/30

Risbøj (DK/DK); Skæring Sandager 54, DK-8250 Egå (DK); ARKNÆS-PEDERSEN, Lars (DK/DK); Bemstoffsvej 13, DK-8260 Viby J (DK).

(21) International Application Number: PCT/DK2004/000696

(74) Agent: PATENTGRUPPEN APS; Arosgården, Aboulevarden 31, DK-8000 Aarhus C (DK).

(22) International Filing Date: 11 October 2004 (11.10.2004)

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
PCT/DK03/00688
10 October 2003 (10.10.2003) DK

(71) Applicant (for all designated States except US): TC ELECTRONIC A/S (DK/DK); Sindalsvej 34, DK-8240 Risskov (DK).

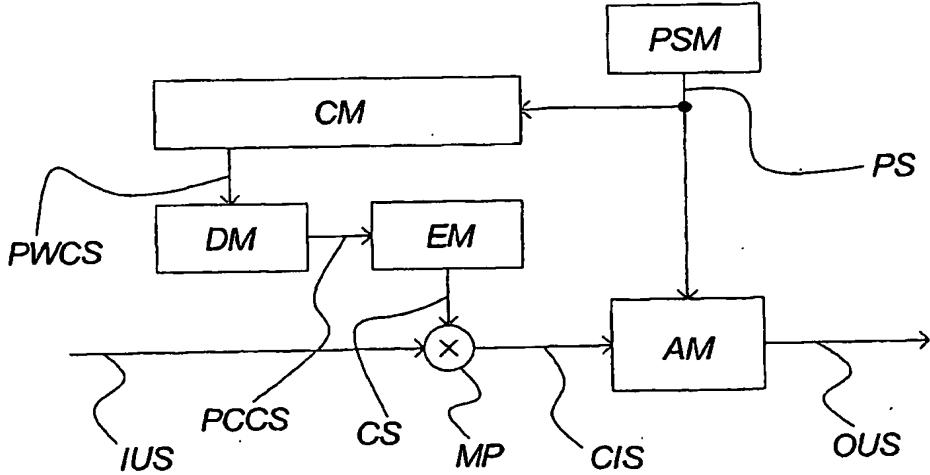
(72) Inventors; and

(75) Inventors/Applicants (for US only): PEDERSEN, Kim,

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,

[Continued on next page]

(54) Title: POWER SUPPLY COMPENSATION



(57) Abstract: The invention relates to an amplifier comprising amplification means (AM) comprising an input and an output, said amplification means (AM) comprising a switching output stage delivering at least one output signal (OUS) via said output, said amplification means being fed by power supply means (PSM) said amplifier further comprising compensation means (CM) providing a compensation signal (CS) derived from the power supply voltage (PSV) of the power supply means (PSM), said compensation signal (CS) comprising a substantially inverse representation of said power supply voltage (PSV) and said compensation signal (CS) being fed to said amplification means (AM).

WO 2005/036735 A1